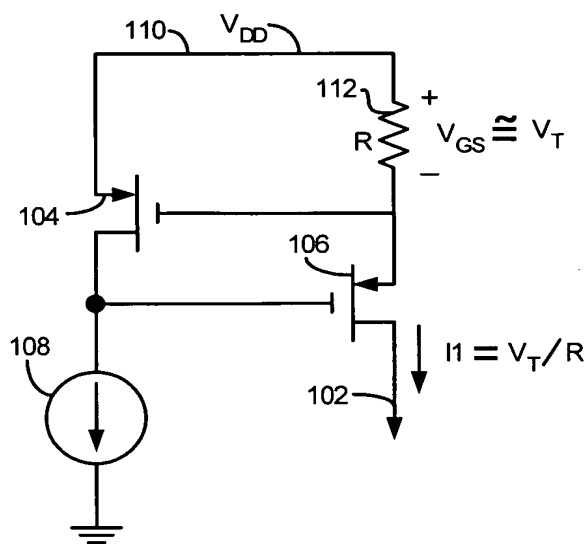


(PRIOR ART)

(PRIOR ART)

100



(PRIOR ART)

(PRIOR ART)

150

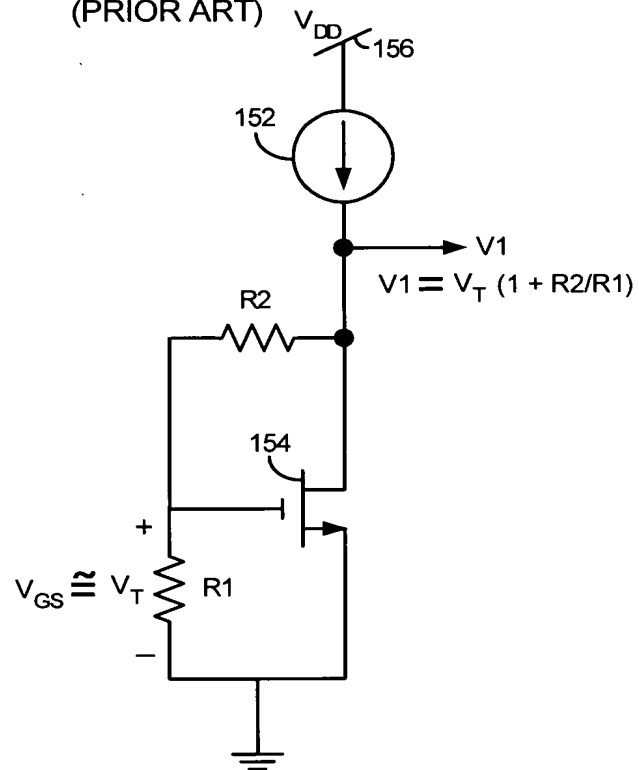


FIG. 2

200

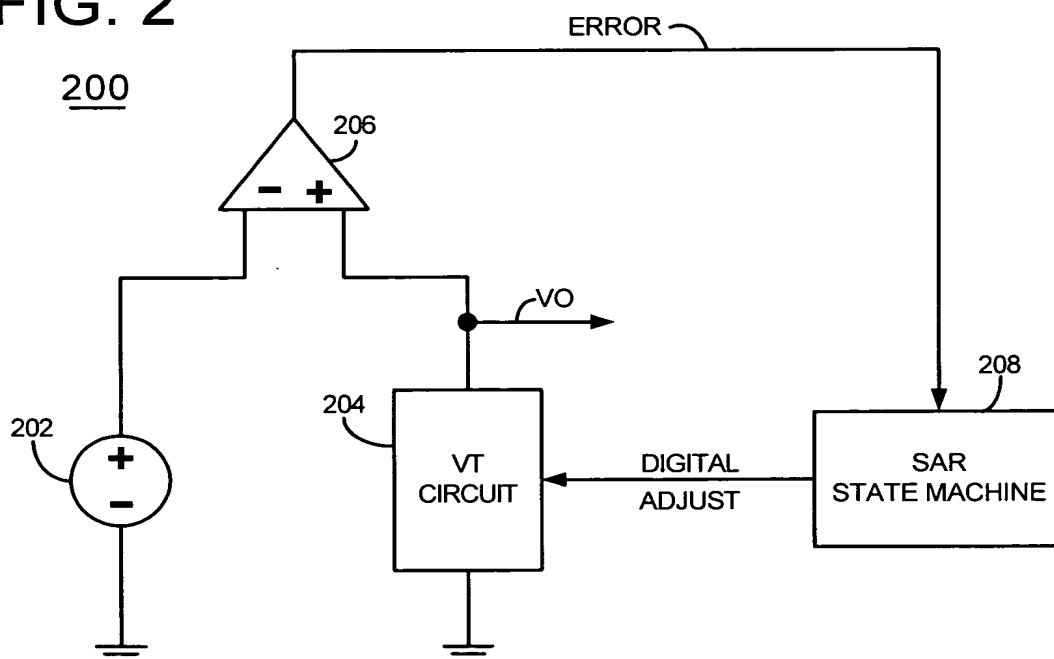


FIG. 3

300

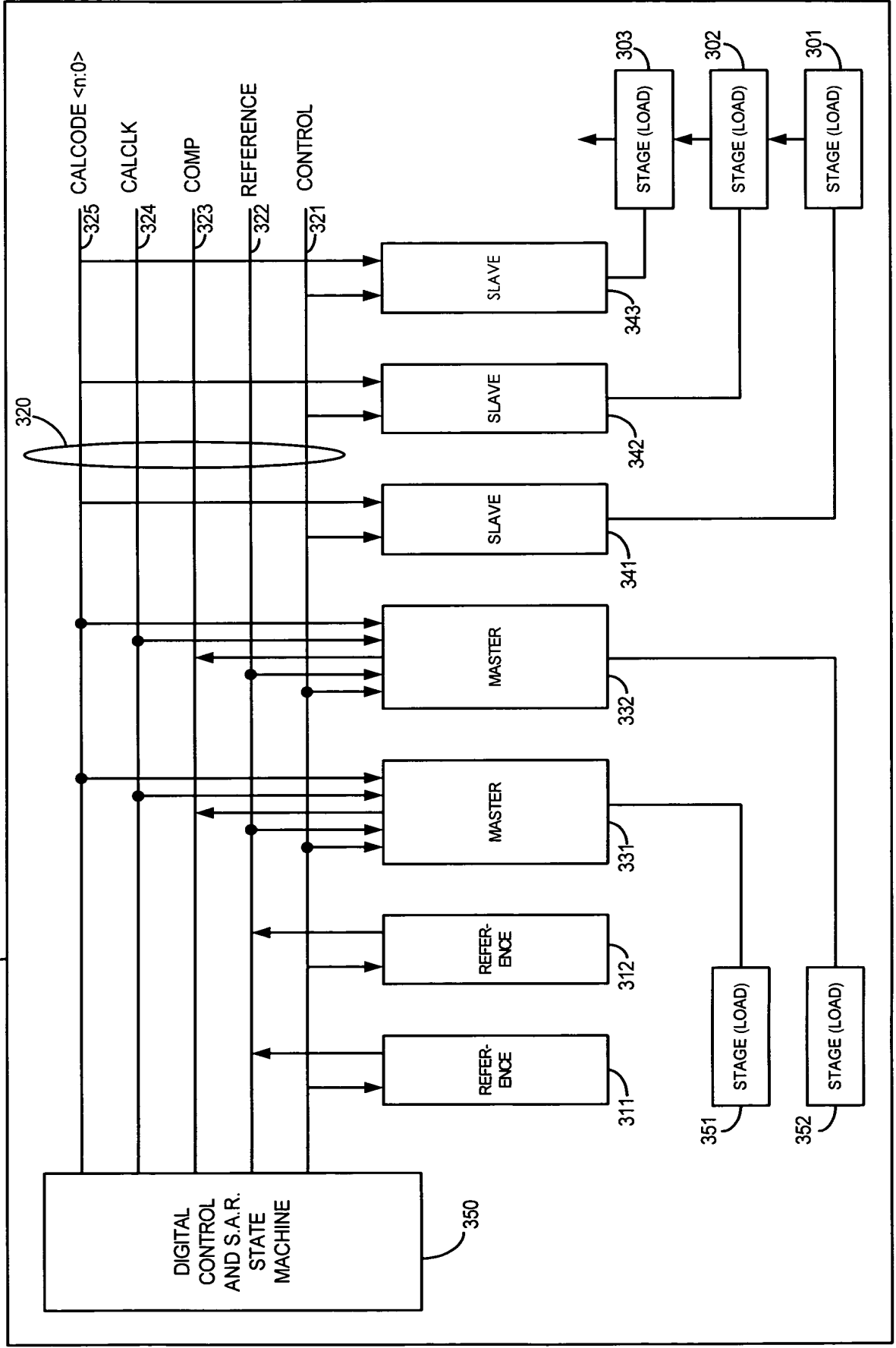


FIG. 4A

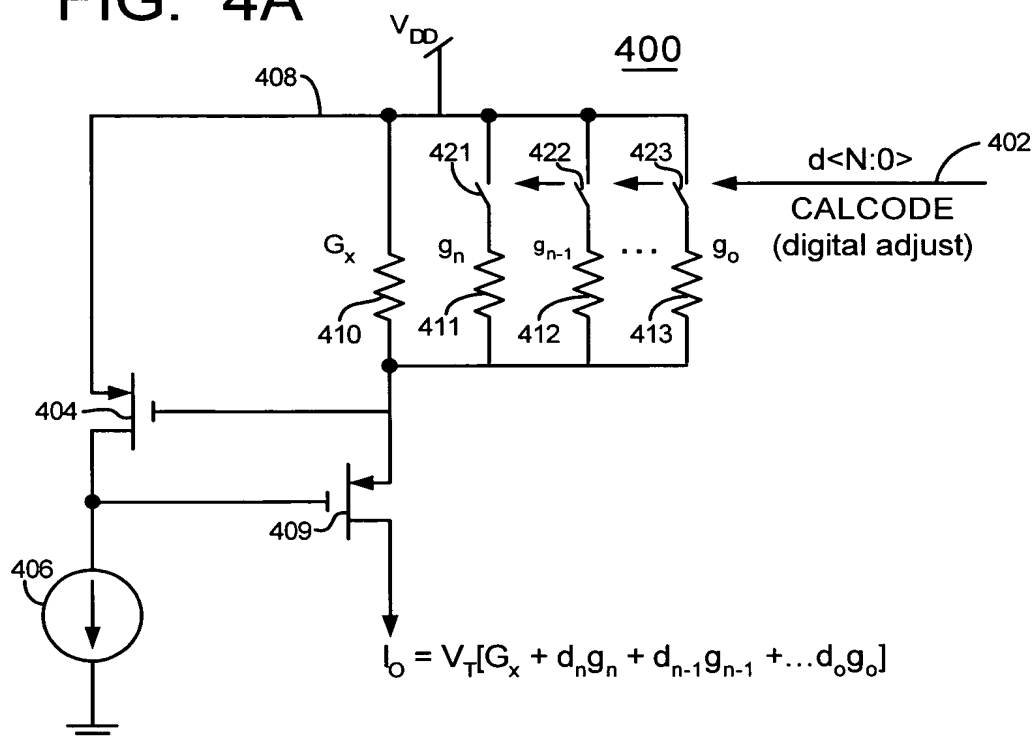


FIG. 4B

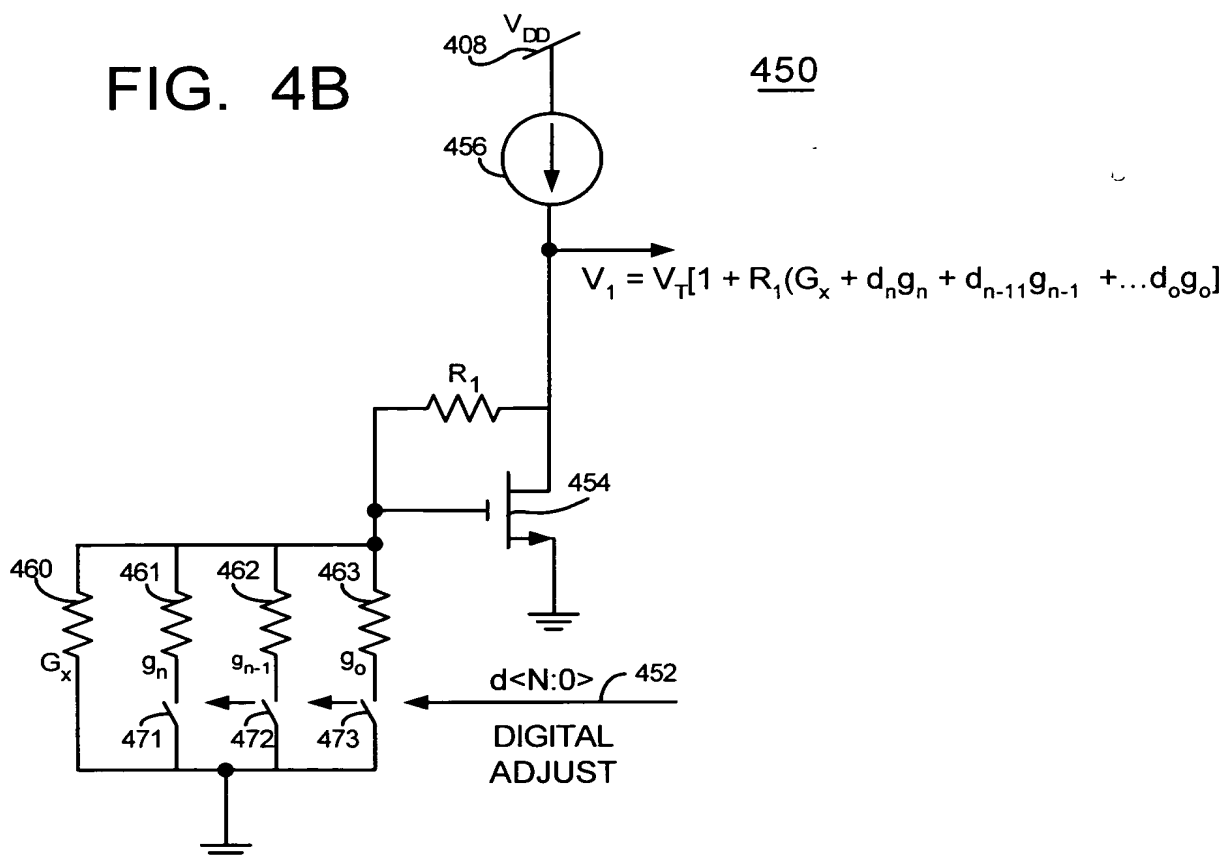
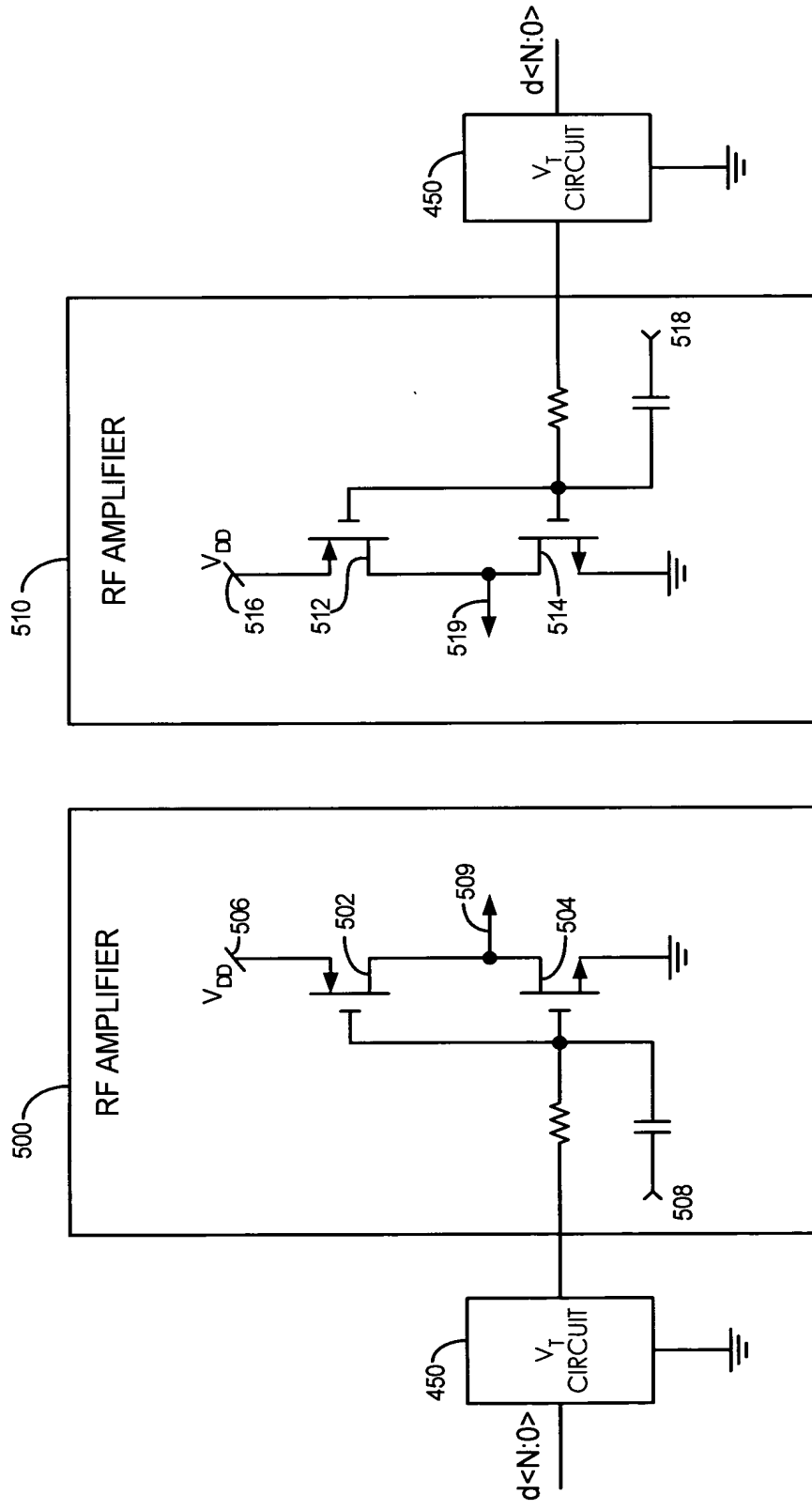


FIG. 5



[illegible]

FIG. 7

331, 332
MASTER

320

322 REFERENCE BUS

323 COMP

321 CONTROL

710

710A

705

450 V_T CIRCUIT

715 LATCH

705A

V_1

325 CALCODE <n:0>

324 CALCLK

FIG. 8 is a block diagram of a slave device. The device includes a V_T CIRCUIT (450) connected to a BIAS FOR RF CIRCUIT (800) and grounded. The V_T CIRCUIT outputs a signal $d<n:0>$ to a LATCH (715). The LATCH is connected to a bus (320) which carries signals 321 (CONTROL), 322 (REFERENCE BUS), 323 (COMP), 324 (CALCLK), and 325 (CALCODE $<n:0>$). The device is labeled 341, 342, 343 SLAVE.

FIG. 9

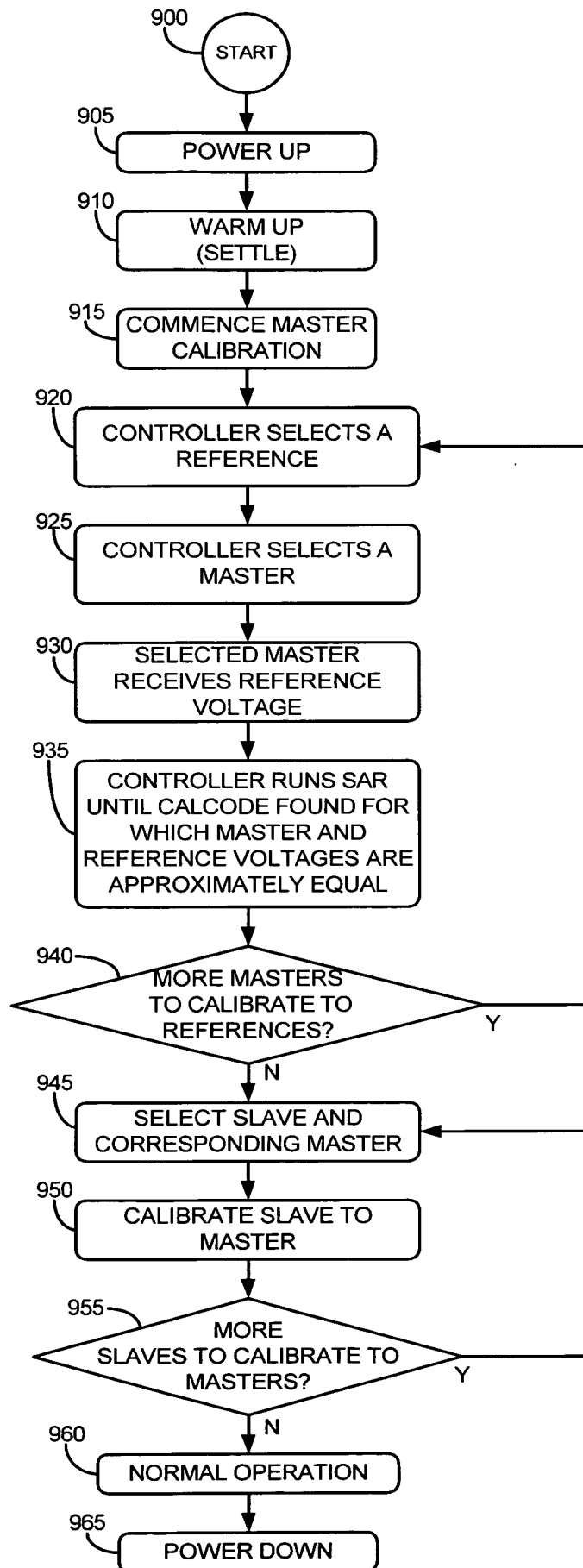


FIG. 10

